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27660 7590 05/28/2004
BURGESS & BEREZNAK LLP
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MOUNTAIN VIEW, CA 94040

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Bradley J. Berezna	(Depositor's name)
	(Signature)
June 22, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/722,792

11/25/2003

Donald Ray Disney

03692.P059DC

7244

TITLE OF INVENTION: METHOD OF FABRICATING A HIGH-VOLTAGE TRANSISTOR WITH A MULTI-LAYERED EXTENDED DRAIN STRUCTURE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional

NO

\$1330

\$300

\$1630

08/30/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
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NHU, DAVID

2818

438-270000

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Burgess & Berezna, LLP
1
2
3

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Power Integrations, Inc.

San Jose, California

Please check the appropriate assignee category or categories (will not be printed on the patent);

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Bradley J. Berezna

06/22/04

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06/28/2004 H0UTEMA2 00000058 10722792

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02 FC:1504
03 FC:8001

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300.00 OP
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